

<b>Notice of References Cited</b>		Application/Control No. 10/604,328	Applicant(s)/Patent Under Reexamination BLANCO ET AL.	
		Examiner Nitin C. Patel	Art Unit 2116	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,541,985	07-1996	Ishii et al.	379/111
*	B	US-6,577,535	06-2003	Pasternak, John H.	365/185.11
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IEEE, 0-7803-7607-2/02; Title, "Managing power and performance for system-on-chip designs using voltage islands" by John M. Cohn et al.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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